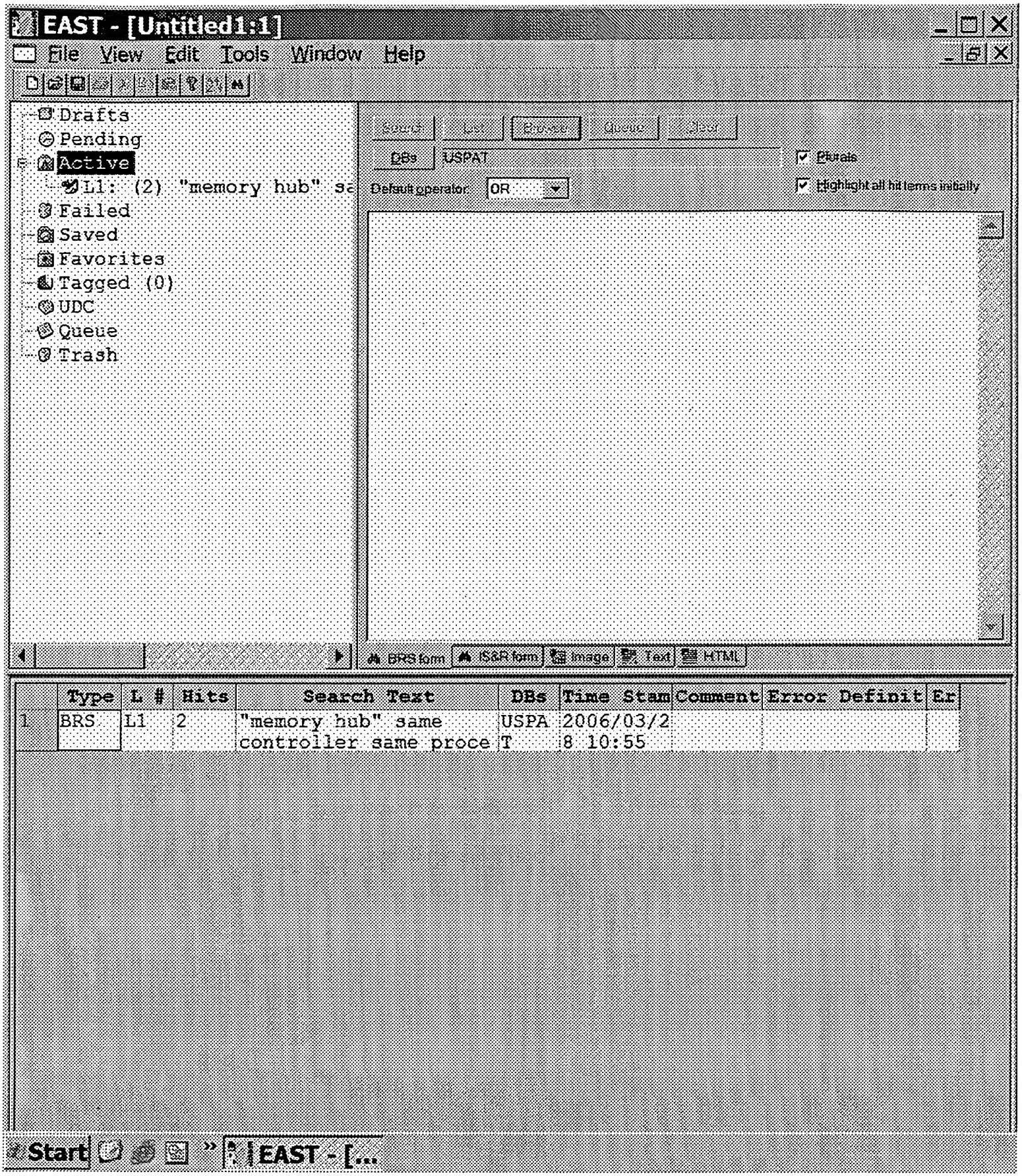
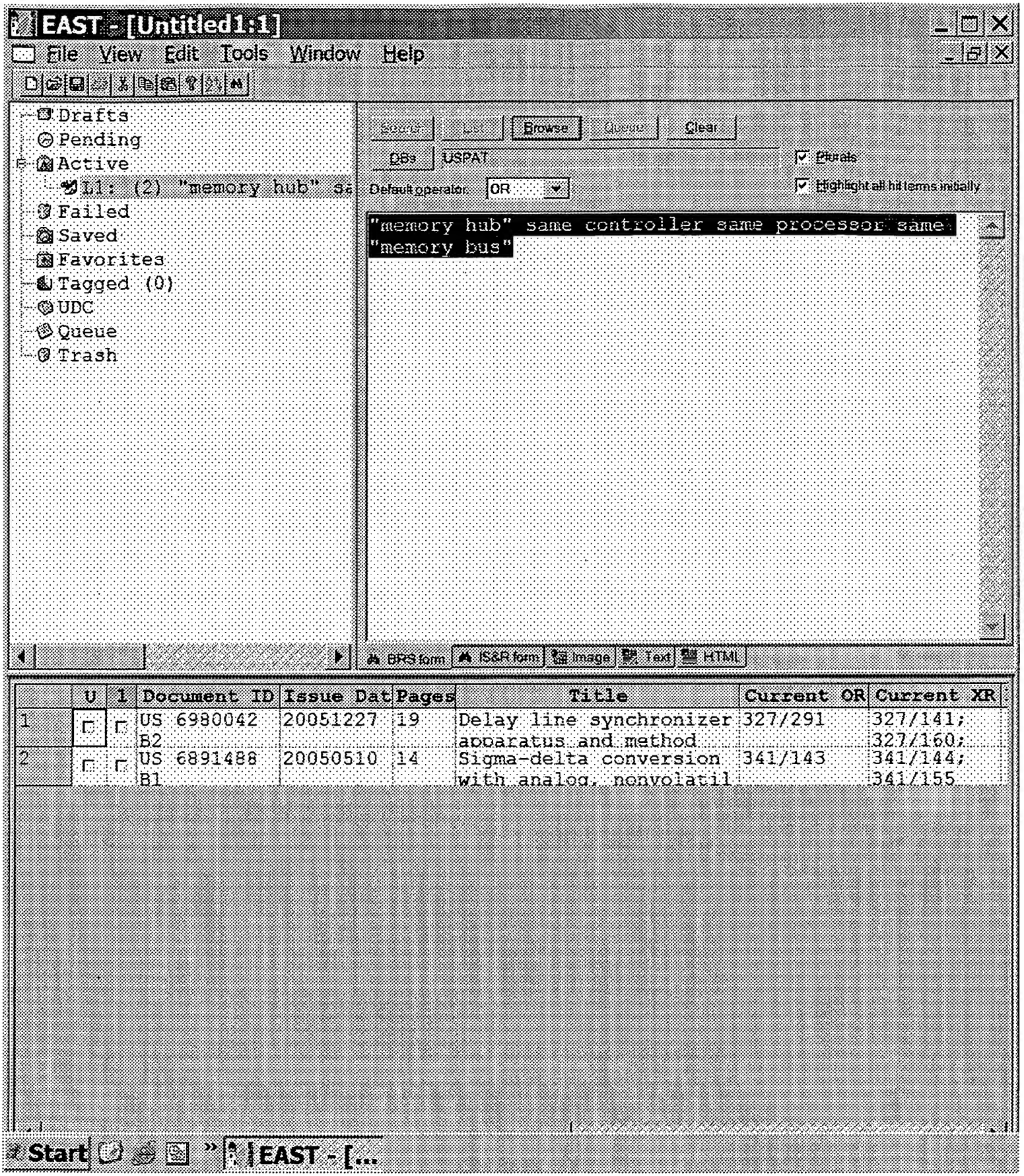

EAST - [Untitled1:1]
File View Edit Tools Window Help
Drafts
Pending
Active
L1: (2) "memory hub" sa
Failed
Saved
Favorites
Tagged (0)
UDC
Queue
Trash
Search
Edit
Browse
Queue
Clear
DBs
USPAT
Plurals
Default operator:
OR
Highlight all hit terms initially
BRS form
IS&R form
Image
Text
HTML
Type L # Hits Search Text DBs Time Stam Comment Error Definit Er
1 BRS L1 2 "memory hub" same controller same proce USPAT 2006/03/28 10:55
Start
EAST - [...


EAST - [Untitled1:1]
File View Edit Tools Window Help
Drafts
Pending
Active
L1: (2) "memory hub" sa
Failed
Saved
Favorites
Tagged (0)
UDC
Queue
Trash
Search
Edit
Browse
Queue
Clear
DBs
USPAT
Plurals
Default operator:
OR
Highlight all hit terms initially
"memory hub" same controller same processor same "memory bus"
BRS form
IS&R form
Image
Text
HTML
U
1
Document ID
Issue Dat
Pages
Title
Current OR
Current XR
1
US 6980042 B2
20051227
19
Delay line synchronizer apparatus and method
327/291
327/141; 327/160;
2
US 6891488 B1
20050510
14
Sigma-delta conversion with analog, nonvolatil
341/143
341/144; 341/155
Start
EAST - [...